

PATENT



ATTORNEY DOCKET NO.: Intel 2207/10121  
ASSIGNEE: INTEL CORPORATION

61A  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Sailesh KOTTAPALLI et al.  
SERIAL NO. : 09/751,762  
FILED : December 29, 2000  
FOR : METHOD FOR CONVERTING PIPELINE STALLS  
CAUSED BY INSTRUCTIONS WITH LONG LATENCY  
MEMORY ACCESSES TO PIPELINE FLUSHES IN A  
MULTITHREADED PROCESSOR WHERE THE  
INSTRUCTIONS ARE RE-EXECUTED UPON  
COMPLETION OF THE ACCESSES (as amended)

GROUP ART UNIT : 2183

EXAMINER : Shane F. GERSTL

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MAR 15 2004

Technology Center 2100

M/S: AMENDMENTS  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

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Dated: March 9, 2004

Signature

*Blanche Guzman-Salmon*  
Blanche Guzman-Salmon

**AMENDMENT**

The following amendments and remarks below are respectfully submitted in response to the Office Action dated December 22, 2003.

**Amendments to the Specification** begin on page 2 of this Amendment.

**Amendments to the Claims** begin on page 3 of this Amendment.

**Remarks/ Arguments** begin on page 7 of this Amendment.